

09:00 - 09:30	REGISTRATION - WELCOME
SESSION 1	SOFTWARE VERIFICATION (INVITED LECTURE)
09:30-10:30	Andrey Rybalchenko (with B. Cook and A. Podelski) <i>Termination Proofs for Systems Code</i>
10:30 - 11:00	COFFEE BREAK
SESSION 2	REACHABILITY ANALYSIS, BISIMULATION
11:00 - 11:30	Giorgio Delzanno, Roberto Montagna <i>On the Reachability Problem for Fragments of Mobile Ambients with Name Restriction</i>
11:30 - 12:00	Mihaela Sighireanu, Tayssir Touili <i>Bounded Communication Reachability Analysis of O-Process Rewrite Systems</i>
12:00 - 12:30	Sibylle Froeschle, Slawomir Lasota <i>Normed Processes, Unique Decomposition, and Complexity of Bisimulation Equivalences</i>
12:30 - 14:30	LUNCH BREAK
SESSION 3	PROCESS CALCULI, CONCURRENCY
14:30 - 15:30	Robin Milner (Joint Express-Infinity-SOS invited lecture) <i>Bigraphs, Multi-local Names and Confluence</i>
15:30 - 16:00	David Teller <i>Concurrent memory management with the pi-calculus</i>
16:00 - 16:30	COFFEE BREAK
SESSION 4	WELL-STRUCTURED SYSTEMS
16:30 - 17:00	Nathalie Bertrand (with Ch. Baier and Ph. Schnoebelen) <i>On Computing Fixpoints in Well-Structured Regular Model Checking, with Applications to Lossy Channel Systems</i>
17:00 - 17:30	Gilles Geeraerts (with J-F. Raskin and L. van Begin) <i>An Efficient Algorithm to Compute the Coverability Set of Petri Nets</i>
17:30 - 17:45	BREAK
SESSION 5	LEARNING (INVITED LECTURE)
17:45 - 18:45	Mahesh Viswanathan <i>Learning to Verify</i>